



Digital VLSI Systems Design: A Design Manual for Implementation of Projects on FPGAs and ASICs Using Verilog (Paperback)

By Seetharaman Ramachandran

Springer, Netherlands, 2014. Paperback. Condition: New. 2007 ed.. Language: English . Brand New Book ***** Print on Demand *****. This book provides step-by-step guidance on how to design VLSI systems using Verilog. It shows the way to design systems that are device, vendor and technology independent. Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards. The reader is taken step by step through different designs, from implementing a single digital gate to a massive design consuming well over 100,000 gates. All the design codes developed in this book are Register Transfer Level (RTL) compliant and can be readily used or amended to suit new projects.



Reviews

This pdf is wonderful. It is definitely simplified but excitement from the 50 percent in the ebook. You wont sense monotony at at any time of your time (that's what catalogues are for relating to should you request me).

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